

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Project on: “Development of a low-profile SMPC with Finemet-based HF magnetics”

[Funding Agency: DeITy, MeITy, Govt. of India]

PI: Prof. Mainak Sengupta,

**Department of Electrical Engineering,
Indian Institute of Engineering Science & Technology (IEST), Shibpur,
Howrah-711 103**

Ref: Advt. No. EE 1855, published in the “Statesman (All Edition)” & “Dainik Statesman”, dated 05.10.2024

[University Project Code: DRC/NAMPET-III(SMPC)[2354]/EE/MS/003/24-25]

Interested candidates are requested to appear for a walk-in interview for the following post in the Department of Electrical Engineering, **Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.**

Name of the post(s): Research Associate-III.

No of post(s) – One (01)

Essential Qualification(s):

1. Ph. D (or equivalent, as per GoI norms) degree in Electrical Engineering from a recognized Indian University/Institute
- Or**
2. ME/ M. Tech with 60% marks or 7.0 CGPA and minimum 3 years of research, teaching and design and development experience with at least one research paper in Science Citation Indexed (SCI) journal as first author.

Desirable Qualification:

1. Proficiency in FEM based electromagnetic and coupled circuit simulation.
2. Sound knowledge of magnetics in power apparatus and converters and their design.
3. Hands on experience in Power Electronic circuit design, analysis, fabrication and testing.
4. Sound knowledge of PCB design software like Dip-trace, Ki-Cad etc..
5. Sound knowledge of open source circuit simulation software like SEQUEL, LT Spice etc.
6. Programming knowledge of VHDL or equivalent on FPGA platform.

Fellowship amount: Rs. 67,000/- + 27% HRA, as per GoI rules, as applicable.

Age Limit: Preferably below 35 years (Upper age limit is relaxable for SC/ST/OBC/Woman and physically handicapped candidates, as per GoI norms for Govt. funded R&D projects).

Duration: 8 months or till the termination of the project whichever is earlier, unless notified otherwise by the funding agency. Interested eligible candidates should email soft copies of the application letter in plain paper, recent bio-data and physical documents, including the above and all marksheets and certificates, in original, shall be verified both before the interview and at the time of joining. The selection will be canceled if any discrepancies are found in the documents at the time of physical verification. Copies of all documents should accompany the email application. All documents should be self-attested.

Mode, Venue and Date of the interview: (i) Walk-in interview at (ii) the Office of the Dept. of Electrical Engineering, IEST Shibpur and (iii) Date & Time of Interview: **15.10.2024 at 04:00 pm.**

N.B.:

1. Soft copies of the application letter, bio-data, mark sheets and certificates should be sent through e-mail in advance by **09.10.2024, 11:59 PM**, to: Prof. Mainak Sengupta, Project Investigator (E-mail id: msg.ee@faculty.iiests.ac.in) with a copy marked to suvarun.ee@faculty.iiests.ac.in. Only candidates applying as above shall be considered for the interview on **15.10.2024.**
2. No claims for TA/DA shall be entertained.
3. All applications must mention a valid e-mail id and mobile number for communicating.
4. Short listing may be done on the basis of soft-copy documents received, if required, before the interview, as per rules.

Dean (R & C)

(W. Code DRC-014/24-25)